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I will probably change texts for next year, most likely to the text by Rabaey et al, which appears to be much better organized for an introduction to VLSI. In sum, Weste 3rd edition might make a good text for a second or third course in VLSI, or a good reference for practitioners in the field, but not a good text for a first course in VLSI.

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Cmos Circuit Design Layout And Simulation Solution Manual

Steven D. Johnson, " Digital Design in a Functional Calculus, " in: G.J. Milne and P.A. Subrahmanyam (eds.), Formal Aspects of VLSI Design, Elsevier Science Publishers B. V. (North-Holland), Amsterdam, 1986 (Proceedings of the 1985 Workshop on VLSI, Edinburgh). Google Scholar

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